



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Gerd Frankowsky et al. Art Unit : 2813
Serial No. : 10/630,632 Examiner : James M. Mitchelle
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Notice of Allowance Date: June 6, 2005
Title : SEMICONDUCTOR CIRCUIT MODULE AND METHOD FOR FABRICATING
SEMICONDUCTOR CIRCUIT MODULES

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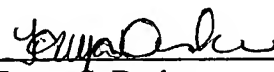
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

COMMENTS ON EXAMINER'S REASONS FOR ALLOWANCE

The applicant notes that there may be other good reasons for the patentability of the allowed claims, and other claims.

Respectfully submitted,

Date: 1/12/06


Tonya S. Drake
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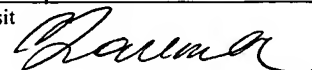
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